



Big-Digits Representation and its Application in Cryptography

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ABSTRACT

The efficiency of number theory based cryptosystems correlates directly to the efficiency of large integer multiplication operation. In this paper, we propose a hybrid of Karatsuba-Classical multiplication algorithm that is based on a Look-up table of "Big-Digits" representation. The Big-Digits representation is a more compact representation compared to the binary representation. Therefore, by using the Big-Digits representation, the number of sub-multiplication operations in a multiplication algorithm will reduce significantly. The results of this study show that the proposed multiplication algorithm, which is based on the Big-Digits representation, is faster than the classical, Karatsuba and the hybrid of Karatsuba-Classical multiplication algorithms in range of the public-key cryptography implementation.

Keywords: Public-key cryptography, Big-Integer calculation, and Karatsuba multiplication algorithm.

1. INTRODUCTION

Number theory based public-key algorithms use multiplication extensively in their operations. Subsequently, many mathematicians and computer scientists have devoted their time in devising efficient multiplying algorithms for large numbers. The best known algorithms for multiplying big numbers in order of complexity are: Schoolbook or classical multiplication algorithm (Knuth (1997)), Karatsuba multiplication algorithm (Karatsuba and Ofman (1963)), Toom-Cook multiplication algorithm (Cook May (1966)), Shönang-Strassen multiplication algorithm (Schönhage and Strassen (1971)), and Fürer (Fürer (2007)) multiplication algorithm. Related

There are two nested loops as shown in Steps 1 and 3 of Algorithm 1 to scan all q digits of the multiplier and p digits of the multiplicand. The maximum length of output C will be $q + p + 1$ and each digit of C will be calculated as follow:

The value of TMP in Step 4, is computed by adding the value of C_{i+j} with the value of $CRRY$ and the result of multiplying the non-zero digit of the multiplier, a_i , (Step 2) with digit b_j from the multiplicand. The values of quotient and remainder are extracted from TMP after divided by b , in Step 5 and are saved in c_{i+j} and $CRRY$ respectively. In this way, all the output digits from the least to the most significant digit are consecutively computed. The complexity of this algorithm is $O(n^2)$, which is slow for applications such as cryptography.

Karatsuba Multiplication Algorithm

Karatsuba multiplication algorithm (Karatsuba and Ofman (1962)) with complexity $O(n^{1.58})$ is a suitable algorithm for multiplying numbers larger than few hundred digits long (Xianjin and Longshu (2007)). This algorithm performs based on two mechanisms; divide and conquer (Cormen *et al.* (2000), Levitin (2002), Mainzer (2007)) and binary splitting (Brent (1976)).

Equations 1 and 2 describe the multiplication operations, which the classical algorithm and Karatsuba algorithm are based on, respectively. The number of partial products in each iteration of the Karatsuba algorithm is three while the classical algorithm has four, which gives Karatsuba algorithm the extra advantage in its calculation.

$$a \cdot b = (a_L \cdot b_L)r^{2n} + (a_R \cdot b_L + a_L \cdot b_R)r^n + (a_R \cdot b_R) \quad (1)$$

$$a \cdot b = (a_L \cdot b_L)r^{2n} + \{(a_R + a_L) \cdot (b_L + b_R) - (a_L \cdot b_L) - (a_R \cdot b_R)\}r^n + (a_R \cdot b_R) \quad (2)$$

Algorithm 2 shows the recursive Karatsuba multiplication algorithm in detail. When the length of the numbers that are being multiplied is 1, the multiplication process is a simple digit by digit multiplication (see Step 2). For numbers larger than 1 digit, the numbers are divided into a lower (a_R) and an upper half (a_L) as shown by Equation 3 before the algorithm is being called again recursively. The algorithm ends after $\log_2 n$ steps.

$$a = a_L \times r^{n/2} + a_R, b = b_L \times r^{n/2} + b_R \quad (3)$$

Since Karatsuba multiplication algorithm run slower for numbers shorter than few hundred digits, some researchers (Von (2002)) had proposed a hybrid approach where Karasuba algorithm is combined with other multiplication methods. Another approach to improve the performance of Karasuba algorithm, is by splitting the numbers into more than 2 segments per iteration. Dan Zuras described 3-way and 4-way variations of the Karatsuba algorithm (Zuras (1994)), and these studies was later extended by M. Sadiq and A. Jawed (Sadiq and Ahmed (2006)) by splitting the numbers into 2-to-ten parts. Related work on Karatsuba algorithm can be found in these literatures (Montgomery (2005), Haining and Hasan (2007), Bernstein (2009)).

Algorithm 2: Karatsuba Multiplication $C=KA(A,B)$

Input: $A = (a_{n-1} \dots a_0)_r$

$B = (b_{n-1} \dots b_0)_r$

Output: $KA(A,B)$

1. **if** $n = 1$
 2. **return** $A \times B$
 3. **else**
 4. $a_L = \left\lfloor \frac{a}{r^{n/2}} \right\rfloor$ *and* $a_R = a \bmod r^{n/2}$ // dividing a into two halves
 - $b_L = \left\lfloor \frac{b}{r^{n/2}} \right\rfloor$ *and* $b_R = b \bmod r^{n/2}$ // dividing b into two halves
 5. $k_0 = KA(a_L, b_L)$
 6. $k_1 = KA(a_R, b_R)$
 7. $k_2 = KA(a_R + a_L, b_R + a_L)$
 8. **return** $k_0 \times r^n + (k_2 - k_1 - k_0) \times r^{n/2} + k_1$
-

2. BIG-DIGITS REPRESENTATION

ZOT representation (Jahani (2009)) is a new representation for integers, which was derived from the binary numbering system. Symbols used in this representation are known as Big-Digits or in short “BD”. The different patterns of “0” and “1” symbols are the foundation of ZOT. These patterns are described as follows:

- **Big-Zero:** A sequence of symbol “0” is identified as Big-Zero or BZ. We represent a BZ with length of n as Z_n . For example, $Z_3 = "000" = 0$ and $Z_1 = "0" = 0$.
- **Big-One:** A sequence of symbol “1” is identified as Big-One or BO. We represent a BO with length of n as O_n . The numerical value of each Big-

One could be obtained by $O_n = \sum_0^{n-1} 2^i$. For example, $O_1 = \sum_0^{1-1} 2^i = 1$ and $O_7 = \sum_0^{7-1} 2^i = 1111111_2 = 127$.

- **Big-Two:** A sequence of symbols “10” with extra symbol “1” at the right side of the sequence is called Big-Two or BT. We represent a BT with length of n as T_n . It is clear from the definition that the length of BT is always odd and its numerical value can be obtained from $T_n = \sum_0^{(n-1)/2} 4^i$. For example, $T_5 = \sum_0^{(5-1)/2} 4^i = 10101_2 = 21$.

Big-Digits is not a unique representation. For example, the binary number of “11111” could be represented by O_5 , $O_4 O_1$, $O_1 O_4$ or $O_3 O_2$. ZOT representation limits these varieties to only one representation. To convert a binary number to the ZOT representation the following rules must be considered.

- **Direction of scanning:** The direction of scanning a binary number to search for a new BD does not matter; however right-to-left is preferred.
- **Valid Big-Digit:** A valid Big-Digit in ZOT representation is a Big-Digit, which cannot be extended with any symbols, either to the left or to the right of the BD. There is one exception; when a Big-One and a Big-Two are next to each other. In this situation the common “1” must belong to BO. For example, the valid representation for “11110101” is $O_4 Z_1 T_5$, not $O_3 T_7$. More detail on ZOT representation can be found in (Jahani 2009).

For coding purposes ZOT is represented as shown by the following example:

$$\underbrace{11111}_{O_5} 000 \underbrace{1010101}_{T_7} 000 \underbrace{11111}_{O_5} = O_{(5,18)} T_{(7,8)} O_{(5,0)}$$

In above example, we can see all BZs disappeared and every non-zero BDs in the representation carry extra one more parameter. The parameters are the length and position of BD in its original binary form. In above example, $T_{(7,8)}$ means there is a BT with length 7 at position 8. This representation will prevent from double scanning of zeros while doing multiplication in ZOT representation.

Implementing Look-Up Table (LUT) in multiplication algorithm has its advantages (Hasan (2000), Mahboob and Ikram (2005), Wen-Ching *et al.* (2008)). To benefit from this technique the ZOT representation is modified to form another variant of ZOT known as ZOT_x , where x is the

upper limit for the maximum size of non-zero BDs in the representation. In this case, the size of the multiplication LUT will be limited to x^2 . The procedure for obtaining ZOT_x representation is similar to the process of obtaining the ZOT representation, except that the maximum length of BDs must be limited to x bits. The following example clarifies this concept.

$$\begin{aligned}
 A &= \overbrace{1111111000010101000111111}^{\text{binary}} = \overbrace{O_{(7,18)} T_{(5,9)} O_{(6,0)}}^{\text{ZOT}} \\
 &= \overbrace{O_{(2,23)} O_{(5,18)} T_{(5,9)} O_{(1,5)} O_{(5,0)}}^{\text{ZOT}_5}
 \end{aligned}$$

3. KARATSUBA MULTIPLICATION ALGORITHM WITH ZOT_x REPRESENTATION

The ZOT_x has less non-zero digits in its representation compared to its original binary representation. Hence, to multiply two ZOT_x numbers, less sub-multiplication operations are required. Classical multiplication algorithm, with some modification, can support the ZOT_x representation; as demonstrated by Algorithm 3.

Algorithm 3: Classical ZOT_x Multiplication Algorithm

Input: $A = (a_p, \dots, a_1, a_0)_2$

$B = (b_q, \dots, b_1, b_0)_2$

Output: $C = CL - ZOT_x(A, B) = (c_{p+q+1}, \dots, c_2, c_1, c_0)_2$

1. $ZOT_x(A) = a^* = a_p^*, \dots, a_1^*, a_0^*$ // where $a_n^* = (a_{nt}^*, a_{nl}^*, a_{np}^*)$
 2. $ZOT_x(B) = b^* = b_q^*, \dots, b_1^*, b_0^*$ // where $b_n^* = (b_{nt}^*, b_{nl}^*, b_{np}^*)$
 3. **for** ($i = 0; i \leq p; i++$)
 4. **for** ($j = 0; j \leq q; j++$)
 5. $c'_{a_i^*+b_j^*} = c'_{a_i^*+b_j^*} + \text{BigDigitMultiplication}(a_i^*, b_j^*);$
 6. $C = \text{Convert to binary}(C')$;
 7. **return** C
-

The first modification is the conversion step, converting binary numbers a and b to ZOT_x representation a^* and b^* (see Steps 1 and 2). In these steps, all BDs such as a_n^* will be denoted by three additional parameters; type denoted by a_{nt}^* , length denoted by a_{nl}^* , and position of BD denoted by a_{np}^* . These conversions are actually the first overhead of the algorithm. The second modification is in Step 5. In this step, the function $\text{BigDigitMultiplication}(a_i^*, b_j^*)$ fetches the result of binary multiplication

of two Big-Digits a_i^* and b_j^* from a pre-calculated LUT. This value will be added to digit $c_{a_{ip}^*+b_{jp}^*}$, where $a_{ip}^* + b_{jp}^*$ addresses the position of the digit. Note that, there is no “carry” from the previous calculation being calculated in Step 5. Therefore, the pre-defined memory for each digit of the output must be big enough to support the summation value in Step 5. The third modification is related to the format of the output. Based on to the memory specified for each digit, the base of the output can be defined. For example if we consider n bytes for each digits. The base of the output is 2^n . In Step 6, the output is converted to binary.

Algorithm 4 shows the hybrid of the Karatsuba algorithm with the Classical- ZOT_x multiplication algorithm. The only difference between Algorithms 2 and 4 is in Step 1. In this step, when the size of the numbers reach the cut-off point value, the Classical- ZOT_x multiplication algorithm will be used for the calculation.

Algorithm 4: Hybrid of Karatsuba and Classical- ZOT_x Multiplication Algorithm

Input: $A = (a_{n-1} \dots a_0)_r$

$B = (b_{n-1} \dots b_0)_r$

Output: $KA - ZOT_x(A, B)$

1. **if** $n \leq \text{cut_off point}$
 2. **return** $(A \times B)_{\text{classical_ZOT}_x}$
 3. **else**
 4. $a_L = \left\lfloor \frac{a}{r^{n/2}} \right\rfloor$ and $a_R = a \bmod r^{n/2}$ // dividing a into two halves
 $b_L = \left\lfloor \frac{b}{r^{n/2}} \right\rfloor$ and $b_R = b \bmod r^{n/2}$ // dividing b into two halves
 5. $k_0 = KA(a_L, b_L)$
 6. $k_1 = KA(a_R, b_R)$
 7. $k_2 = KA(a_R + a_L, b_R + a_R)$
 8. **return** $k_0 \times r^n + (k_2 - k_1 - k_0) \times r^{n/2} + k_1$
-

In the following section, we compare the efficiency of the proposed multiplication algorithm with the existing classical (CL), Karatsuba (KA) and hybrid of Karatsuba-Classical (KA-CL) multiplication algorithms in range of the public-key cryptography algorithms.

4. RESULTS

According to (Jahani (2009)), the Hamming weight for 32 bits to 32 Kbits random numbers (Matsumoto and Nishimura (1998)) is about 20% while the Hamming weight for binary number is 50%. Therefore, theoretically the number of partial multiplication for classical and Classical-

ZOT_x multiplication algorithm will be about $0.25n^2$ and $0.04n^2$, respectively. Subsequently, the classical- ZOT_x multiplication algorithm should be about 6.25 times faster than the classical multiplication algorithm. Because of the overhead in converting the binary numbers to the ZOT_x representation and the call to the function *BigDigitMultiplication*, the actual speed-up ratio is less than what is being speculated above. This paper investigates the effectiveness of combining the Karatsuba algorithm with the Classical- ZOT_x multiplication algorithm.

Random numbers that are being represented by Big-Digits have special distribution, which will help in determining the optimized size for the lookup table. Table 1 shows that with higher value of x (the maximum length of Big-digits), more numbers can be converted to ZOT_x . Result in Table 1, which are based on the 50 different 8 Kbits random numbers, indicates that 99% of random number has less than 8 non-zero symbols. In general, depending on the application and available memory, we can increase or decrease the value of x . However, the proposed range ($x=7$) should covers 99% of the numbers. The other 1% of the numbers can be segmented into a few Big-Digits with length less than 7 bits.

TABLE 1: Distribution percentage of Big-Digits in a random numbers (8 Kbits)

x	1	2	3	4	5	6	7
Percentage	37%	67%	84%	92%	96%	98%	99%

Table 2 shows the measured execution time for each algorithm (CL, KA, KA-CL and KA- ZOT_x) within the range of 32 bits to 8 Kbits. The number of random numbers used for each test is 50 and the cut-off points were determined by experimenting with the KA-CL algorithm. The proposed algorithm was tested under the same conditions as other algorithms with the same cut-off points. The machine specification used in the experiment is as follows: AMD Phenom (TM) 9950 Quad-core CPU 2.6 GHz, 3.25GB RAM, Windows XP Professional version 2002 (Service Pack 3) OS and Dev-C++ version 4.9.9.2 compiler.

Table 2 shows that the hybrids algorithms have different cut-off points depending on the length of the number. The cut-off point value increases continuously against the length of numbers within the range of 32 to 128 bits. For numbers in the range of 1 Kbits up to 8 Kbits, the cut-off point stable at 16 bits.

TABLE 2: Execution time (msec) of multiplication algorithms

Algorithm	Length of numbers (bits)								
	32	64	128	256	512	1024	2048	4096	8192
CL	0.007	0.023	0.083	0.344	1.27	4.9	19.2	76.9	308.5
KA	0.021	0.064	0.193	0.592	1.77	5.4	15.9	48.3	142.9
KA-CL	0.008	0.024	0.078	0.267	0.82	2.5	7.6	23.3	71.4
ProposedAlgorithm ($x=7$)	0.005	0.010	0.020	0.094	0.28	1.4	4.1	13	33.3
Cut-offPoint	16	32	64	32	32	16	16	16	16

The results show that the performance of $KA-ZOT_x$ multiplication algorithm is better than CL, KA and KA-CL. The speed of $KA-ZOT_7$ is about 1.4 times faster than CL and increases to 9.2 times faster for 8 Kbits numbers. Comparing the execution speed between $KA-ZOT_x$ and KA, tells us that $KA-ZOT_x$ is about 4.2 times faster for 32 bits number and increases to about 4.3 times faster for 8 Kbits numbers, with some fluctuation in between. Figure 1 also indicates that $KA-ZOT_x$ is relatively faster than KA-CL. $KA-ZOT_x$ is about 1.6 times faster to 2.9 times faster for multiplying numbers in the range of 32 bits to 8Kbits.

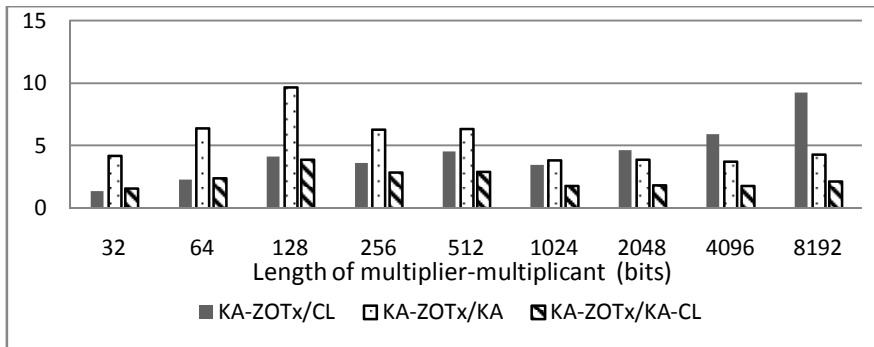


Figure 1: Comparison of $KA-ZOT_x$ multiplication algorithm against CL,KA and KA-CL multiplication algorithms

5. CONCLUSION

In this paper, we proposed a new hybrid multiplication algorithm, combining the Karatsuba multiplication algorithm with the ZOT_x multiplication algorithm. The proposed Karatsuba- ZOT_x (with $x = 7$) outperforms all other tested algorithms. The result indicated that Karatsuba- ZOT_x algorithm is about 1.6 (for 32 bits numbers) to 2.9 times faster (for 8192 bits numbers) against the best existing Karatsuba-Classical algorithm.

The finding from this paper indicates that the proposed algorithm is currently the most suitable multiplication algorithm for the use in existing public-key cryptosystems.

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